

IMAGE PROCESSOR

Publication number: JP2000242249

Publication date: 2000-09-08

Inventor: KONNO KAZUHITO; TAKEUCHI KENJI; MASUBUCHI YUKIO

Applicant: FUJI XEROX CO LTD

Classification:

- International: H04N1/40; H04N1/50; H04N1/56; H04N1/40; H04N1/50; H04N1/56; (IPC1-7): G09G5/00; B41J5/30; B41J21/00; G06F3/12; G06T1/00; H04N1/23

- european: H04N1/40; H04N1/50D; H04N1/56

Application number: JP19990042658 19990222

Priority number(s): JP19990042658 19990222

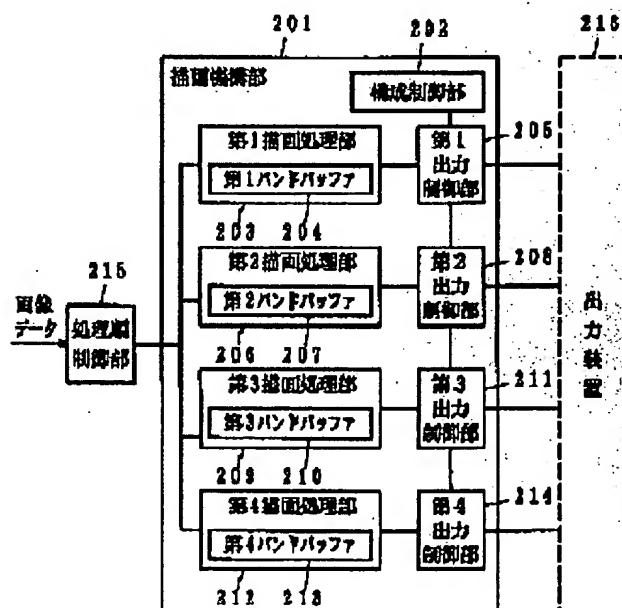
Also published as:

US6529289 (B1)

[Report a data error here](#)

Abstract of JP2000242249

PROBLEM TO BE SOLVED: To provide an image processor which can be used regardless of the mechanism of an output device connected to the processor. **SOLUTION:** A constitution control section 202 of the processor sets the connecting configuration and the output system of first to fourth output control sections 205, 208, 211 and 214 depending on the constitution, the connecting method and inputted picture data of an output device 216. Moreover, a processing order control section 215 is linked with the section 202 and constitutes data from inputted picture data for each band so that the order of the data becomes corresponding to the constitution of first to fourth plotting processing sections 203, 206, 209 and 212 being controlled by the section 202. Then, a control is made so that the data for each band are inputted to the sections 203, 206, 209 and 212 in accordance with the order of processes. Thus, the image processor is adaptive to the case where various output devices are connected to the processor.



Data supplied from the esp@cenet database - Worldwide